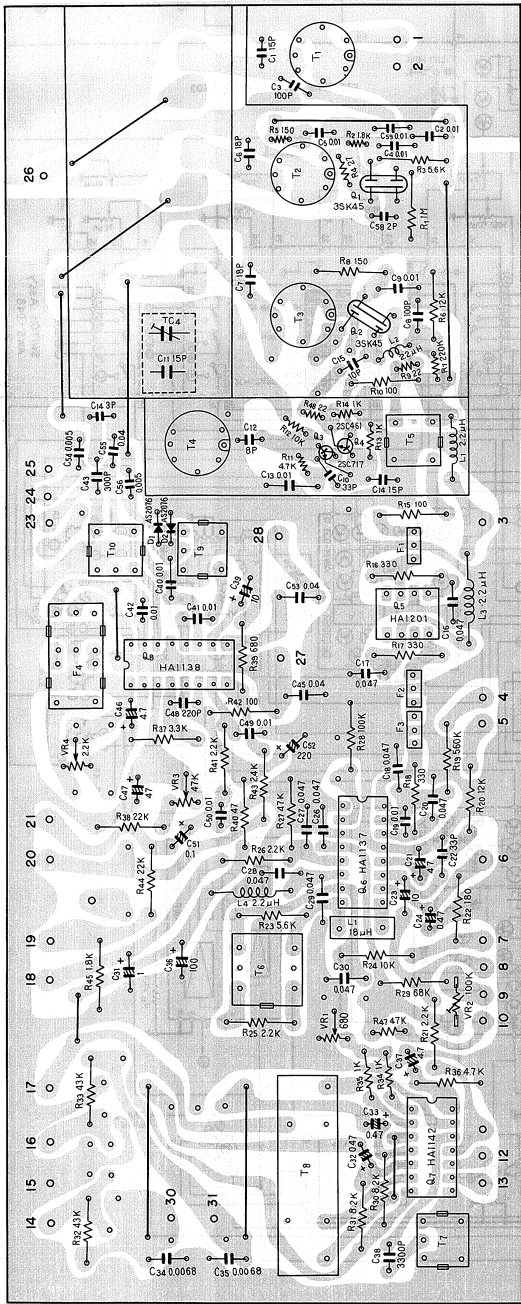
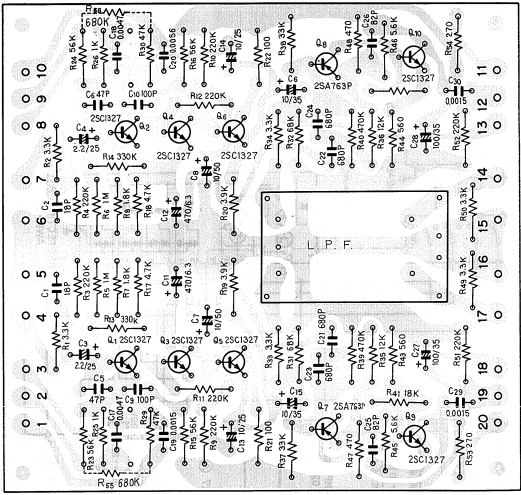


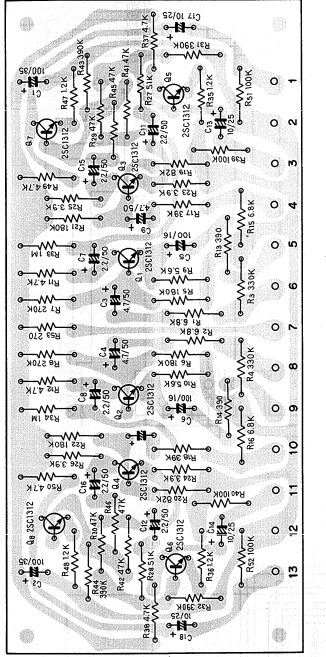
Tuner ass'y AWE-027



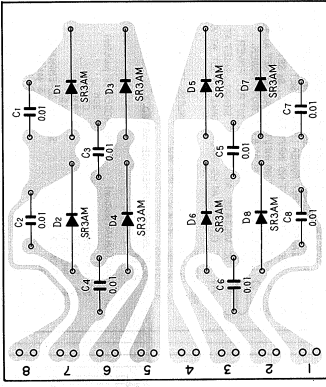
Equalizer amp ass'y AWF-010



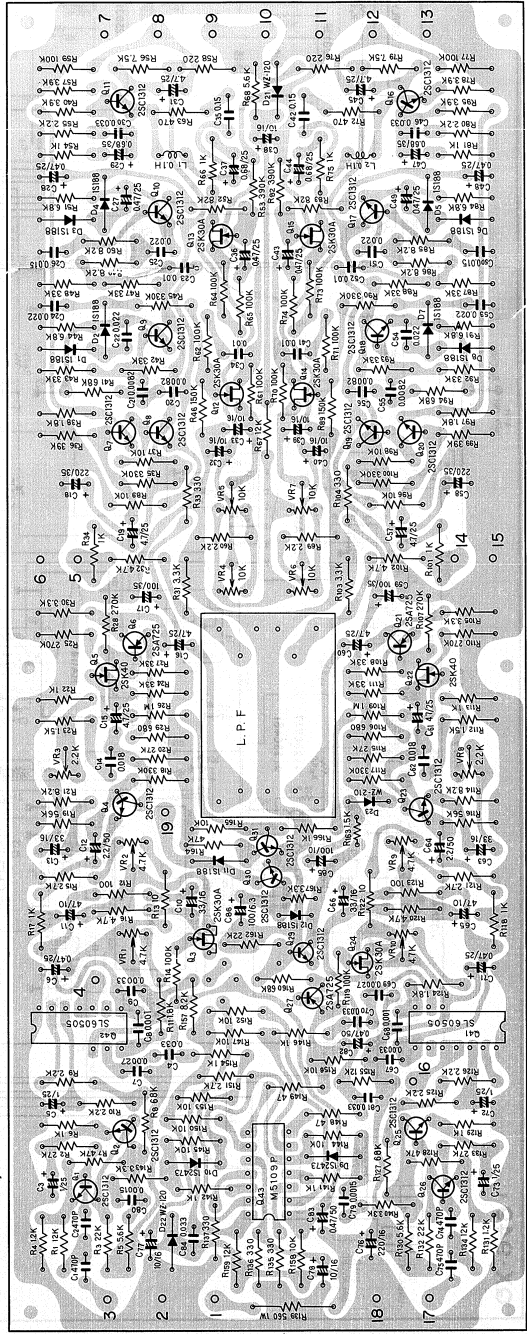
Matrix ass'y AWM-048



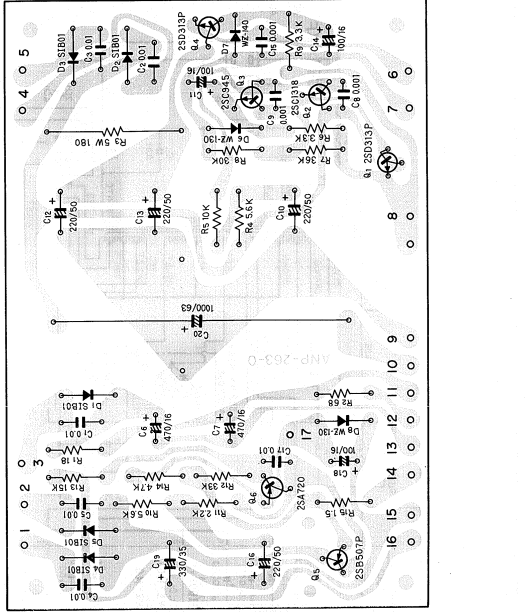
Power supply ass'y B AWR-039



Sub channel circuit ass'y AMX-051

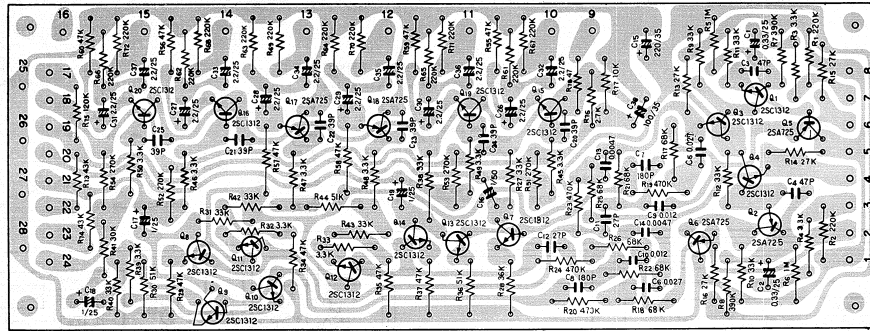


Power supply ass'y A AWR-038

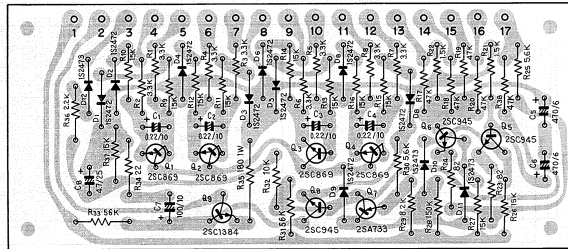


QUADRAPHONIC RECEIVER
QX-949
 FV FUW

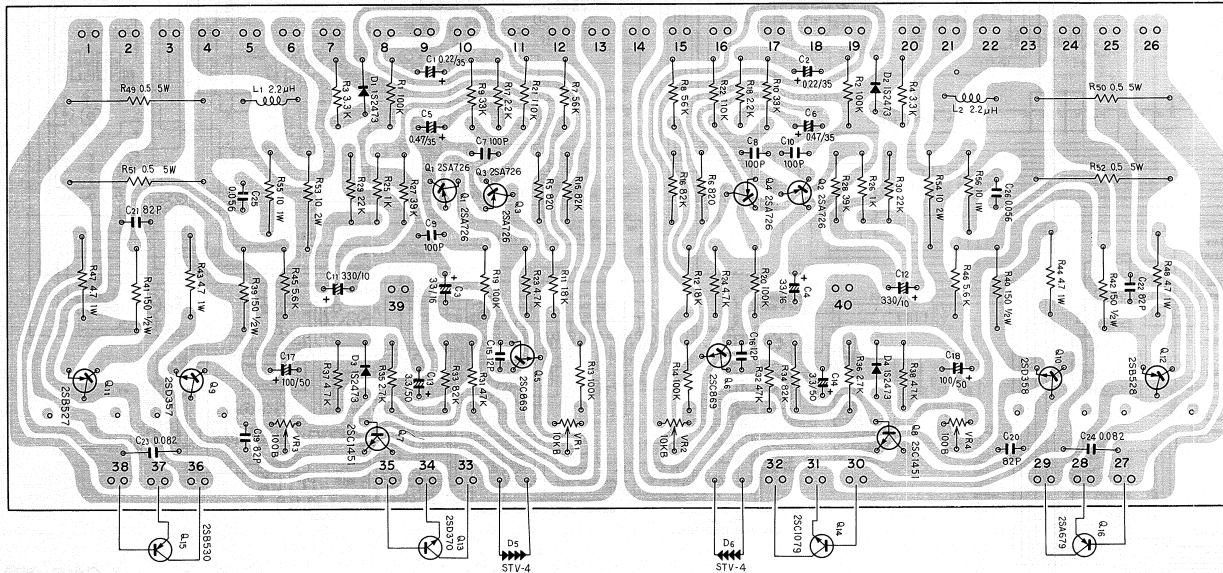
Decoder ass'y AWM-031



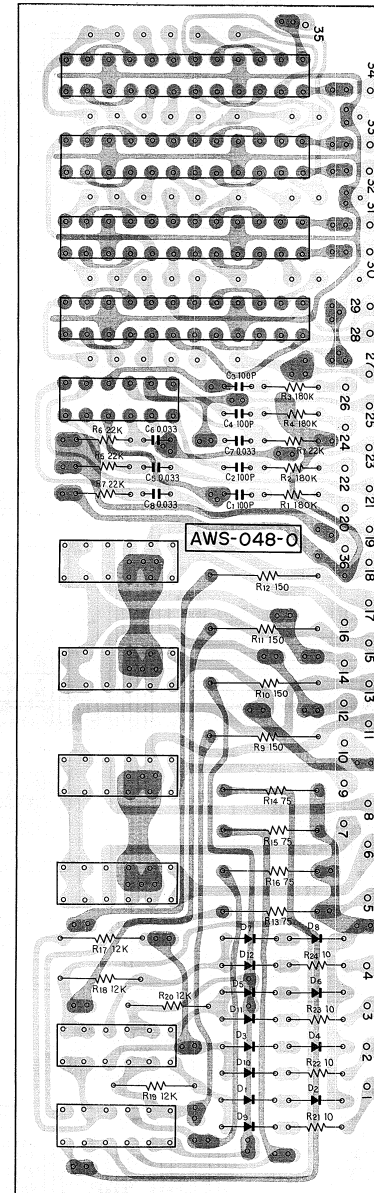
Protection circuit ass'y AWM-049



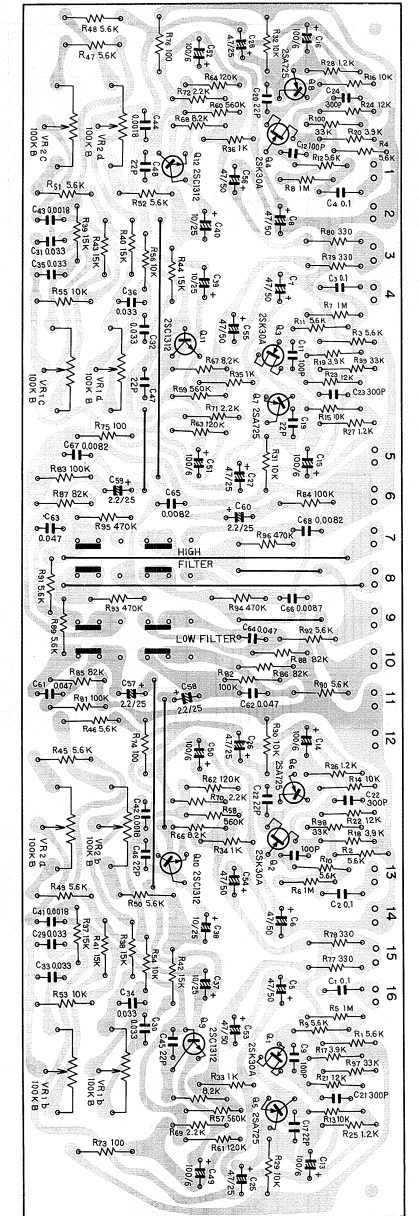
Power amp ass'y AWH-027

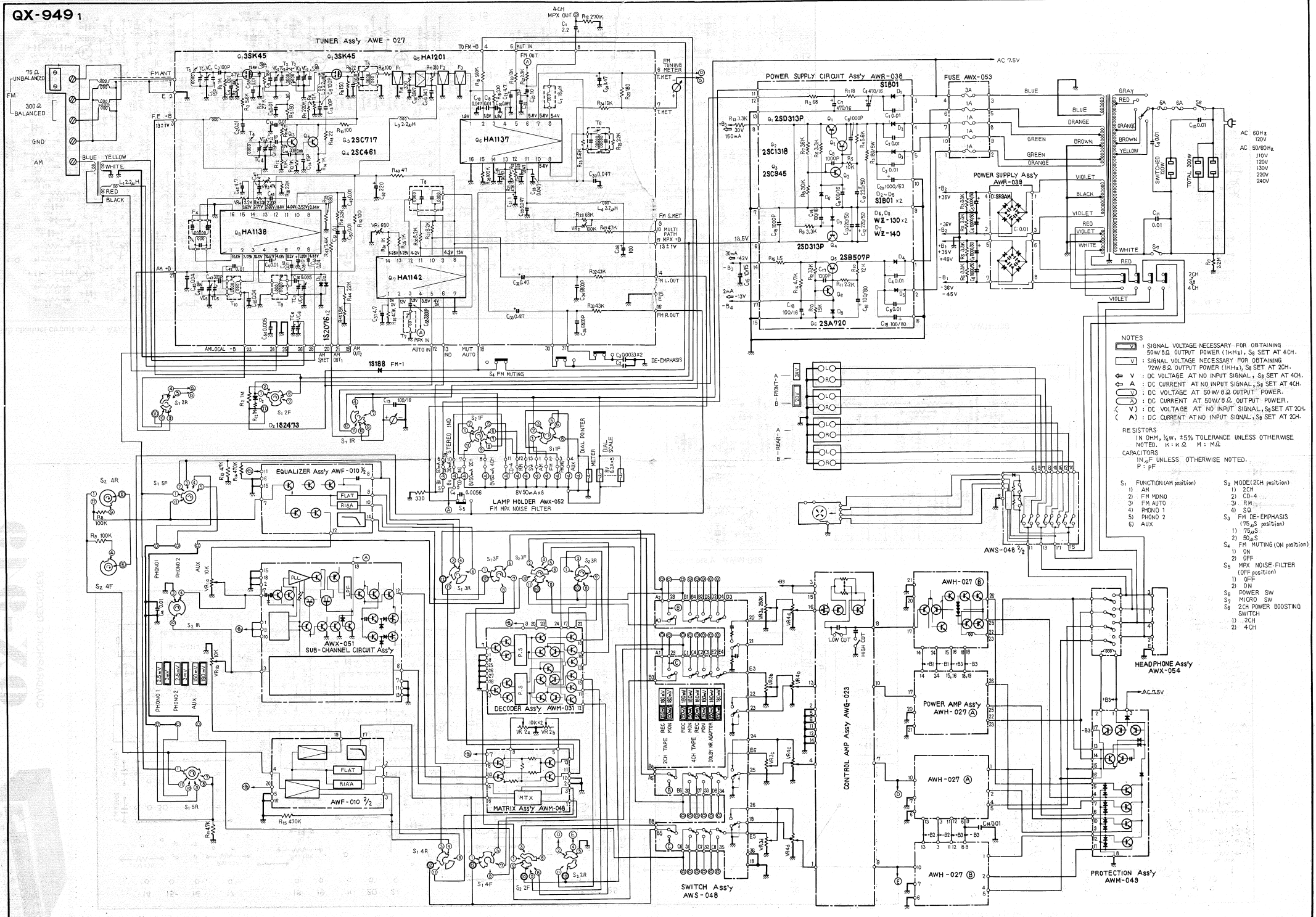


Switch ass'y AWS-048



Control amp ass'y AWG-023





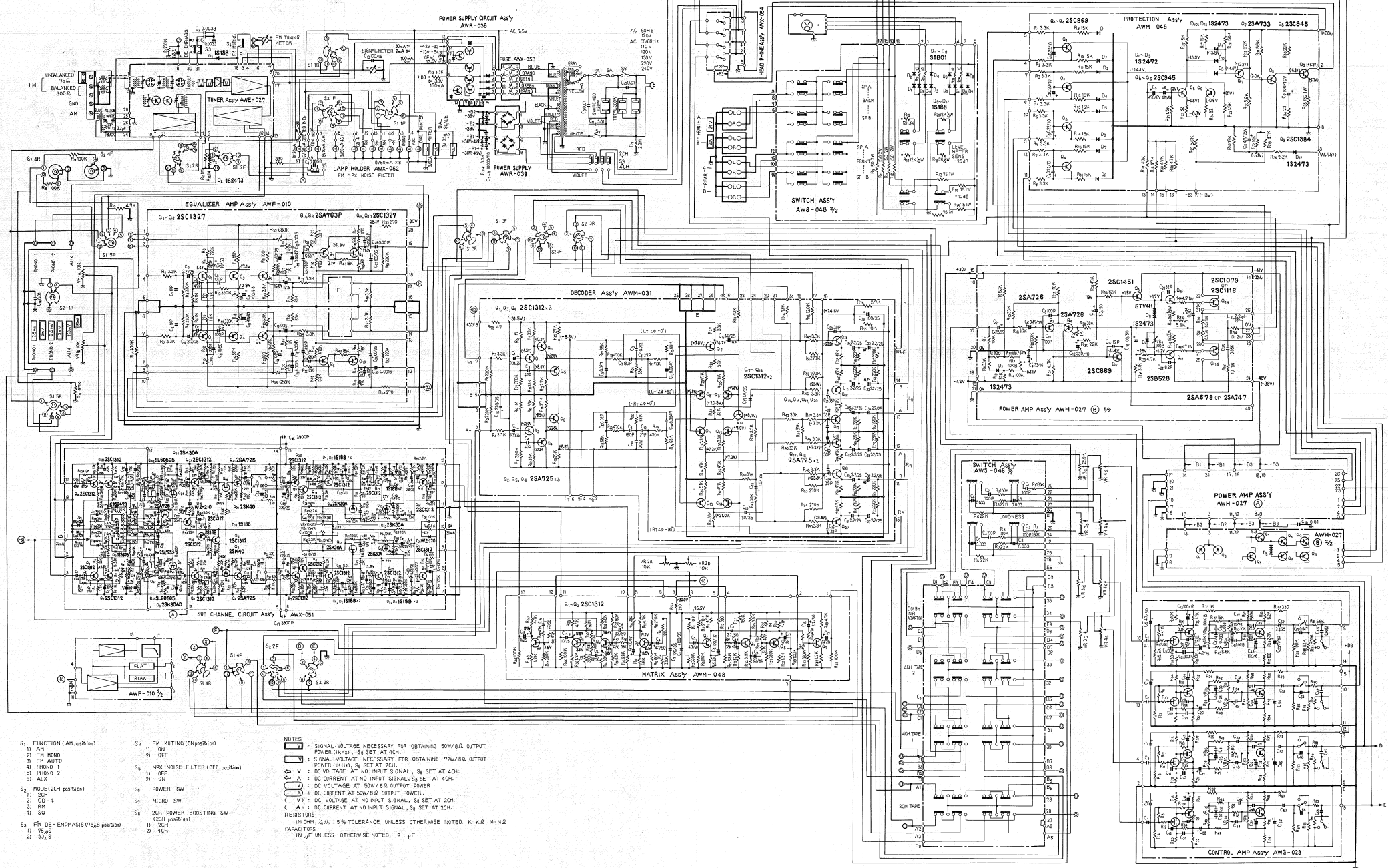
NOTES

- ⊖ : SIGNAL VOLTAGE NECESSARY FOR OBTAINING 50W/8Ω OUTPUT POWER (1kHz), S₈ SET AT 4CH.
- ∇ : SIGNAL VOLTAGE NECESSARY FOR OBTAINING 72W/8Ω OUTPUT POWER (1kHz), S₈ SET AT 2CH.
- ∅ : DC VOLTAGE AT NO INPUT SIGNAL, S₈ SET AT 4CH.
- ⊕ : DC CURRENT AT NO INPUT SIGNAL, S₈ SET AT 4CH.
- ⊖ : DC VOLTAGE AT 50W/8Ω OUTPUT POWER.
- ∅ : DC CURRENT AT 50W/8Ω OUTPUT POWER.
- (∇) : DC VOLTAGE AT NO INPUT SIGNAL, S₈ SET AT 2CH.
- (⊕) : DC CURRENT AT NO INPUT SIGNAL, S₈ SET AT 2CH.

RESISTORS
IN OHM, 1/4W, ±5% TOLERANCE UNLESS OTHERWISE NOTED. K: kΩ M: MΩ

CAPACITORS
IN μF UNLESS OTHERWISE NOTED. P: pF

- S₁ FUNCTION (AM position)**
- 1) AM
 - 2) FM MONO
 - 3) FM AUTO
 - 4) PHONO 1
 - 5) PHONO 2
 - 6) AUX
- S₂ MODE (2CH position)**
- 1) 2CH
 - 2) CD-4
 - 3) RM
 - 4) SS
- S₃ FM DE-EMPHASIS (75μS position)**
- 1) 75μS
 - 2) 50μS
- S₄ FM MUTING (ON position)**
- 1) ON
 - 2) OFF
- S₅ MPX NOISE-FILTER (OFF position)**
- 1) OFF
 - 2) ON
- S₆ POWER SW**
- S₇ MICRO SW**
- S₈ 2CH POWER BOOSTING SWITCH**
- 1) 2CH
 - 2) 4CH



- S1 FUNCTION (AM position)
 - 1) AM
 - 2) FM MONO
 - 3) FM AUTO
 - 4) PHONO 1
 - 5) PHONO 2
 - 6) AUX
- S2 MODE (2CH position)
 - 1) 2CH
 - 2) CD-4
 - 3) AM
 - 4) SG
- S3 FM DE-EMPHASIS (75µs position)
 - 1) 75µs
 - 2) 40µs
- S4 FM MUTING (0 position)
 - 1) ON
 - 2) OFF
- S5 MPX NOISE FILTER (OFF position)
 - 1) OFF
 - 2) ON
- S6 POWER SW
- S7 MICRO SW
- S8 2CH POWER BOOSTING SW (2CH position)
 - 1) 2CH
 - 2) 4CH

NOTES

- (V): SIGNAL VOLTAGE NECESSARY FOR OBTAINING 50W/8Ω OUTPUT POWER (I_{FM}) = S₈ SET AT 4CH.
- (V): SIGNAL VOLTAGE NECESSARY FOR OBTAINING 72W/8Ω OUTPUT POWER (I_{MH}) = S₈ SET AT 2CH.
- (I): DC CURRENT AT NO INPUT SIGNAL, S₈ SET AT 4CH.
- (I): DC VOLTAGE AT 50W/8Ω OUTPUT POWER.
- (I): DC CURRENT AT 50W/8Ω OUTPUT POWER.
- (V): DC VOLTAGE AT NO INPUT SIGNAL, S₈ SET AT 2CH.
- (I): DC CURRENT AT NO INPUT SIGNAL, S₈ SET AT 2CH.

RESISTORS
 Ω OHM, kΩ, ±5% TOLERANCE UNLESS OTHERWISE NOTED. K: KΩ, M: MΩ.

CAPACITORS
 n: nF UNLESS OTHERWISE NOTED. p: pF